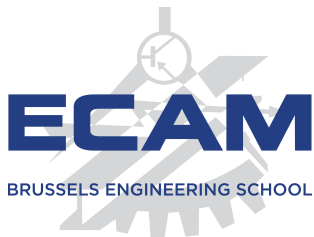


# Virtualisation - Video game system emulation

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January 7, 2020



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# Objectives

At the end of this presentation about *emulation*, you should be able to understand:

- ▶ The *challenges*
- ▶ The *operation*
- ▶ The main *disadvantages* and *limits* (performances, costs, ...)
- ▶ The *Game Boy* example

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# What is Emulation

*"An emulation is an imitation of software or hardware, which gives you the ability to use a product, even if you don't have the original. An emulation has to imitate the real product as much as possible and even replicate known errors."*

*Troels Ynddal, Mads Ynddal, Asger Lund Hansen [2016]*

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# Types of emulation 1/2

- ▶ Software emulation

It simulates the hardware with *a layer of software* on top of *a general hardware*.

- ▶ Hardware emulation

It simulates the hardware with *a layer of dedicated (programmable) hardware* like coprocessor or FPGA.

## Types of emulation 2/2

	<b>Software emulation</b>	<b>Hardware emulation</b>
Widespreadness	+	-
Price	-	+
Efficiency	-	+

This is a tendency to use with caution as it depends on the system to emulate.



# Challenges of the Emulation

- ▶ Emulate the hardware

It's easier to emulate a hardware if we have *a similar hardware* with *the same set of instructions* and *the same handling errors*.

- ▶ Emulate the OS

It needs an OS with *an identical process scheduling queue* and *same environment* for the programs to run.

- ▶ Respond like the original

For gaming, it needs *the same behaviour* than the original console. It needs *the same "speed"* for the frames and for the gamer inputs responses.

# Limits of emulation

Emulation depends on two main points of the system to emulate:

- ▶ Knowledge

The first step is *reverse engineering*, *the understanding* of *original device* operation (PCB, chips, software, OS, etc.).

- ▶ Hardware power

The hardware must be *powerful enough* to handle the OS and the gaming emulation. The emulation's application needs to run faster than the original; the system *bottleneck must be the emulated clock*.

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# Consoles emulation status

Console	Reverse-engineering	Powerfull hardware
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## Console without OS

Game Boy	✓ - Lot of doc.	✓ - Old hardware
Playstation 2	✓ - Lot of doc.	✓ - Old hardware

## Console with OS

Switch	✓ - No security	✓ - No powerful hardware
Playstation 3	✗ - Security	○ - Actual hardware
Playstation 4	✗ - Security	○ - Actual hardware
Xbox 360	✗ - Lost doc.	✓ - Old hardware
Xbox One	✗ - Security	○ - Actual hardware

Difficulty color guide: **easy**, **medium**, **hard**

# Playstation 1/2

## Emulation of Playstation 1 with Playstation 2

- ▶ Dedicated hardware

The "Fat" Playstation 2 has a coprocessor to run PS1 games. The emulation is also possible because they share a similar hardware architecture.

- ▶ Dedicated software

The "Slim" Playstation 2 uses software to emulate the PS1. The hardware of the "Slim" PS2 is more powerful than the "Fat" PS2.

## Playstation 2/2



## Emulation of Xbox 360 with Xbox One

- ▶ Dedicated hardware

The Xbox One has specific chips for Xbox 360 emulation: A Xbox 360 VGPU dedicated to Xbox 360 emulation, etc.

- ▶ Dedicated software

The Xbox One has a dedicated software to emulate the Xbox 360 OS.

- ▶ *Recompile* the game

The games are recompiled to match the Xbox One architecture.

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## Example: Game Boy

- ▶ Software emulation: mGBA, VisualBoyAdvance, etc.

It uses software on computer to emulate the GB/GBA hardware.

- ▶ Hardware emulation: Analogue pocket

It uses FPGA to emulate the hardware of the GB/GBA.

- ▶ Backwards compatibility: GB/GBC/GBA

The GB and GBC shares the same hardware. The GB/GBC games are backwards compatible due to a coprocessor in the GBA.

# Reverse engineering

## Specific hardware of the Game Boy

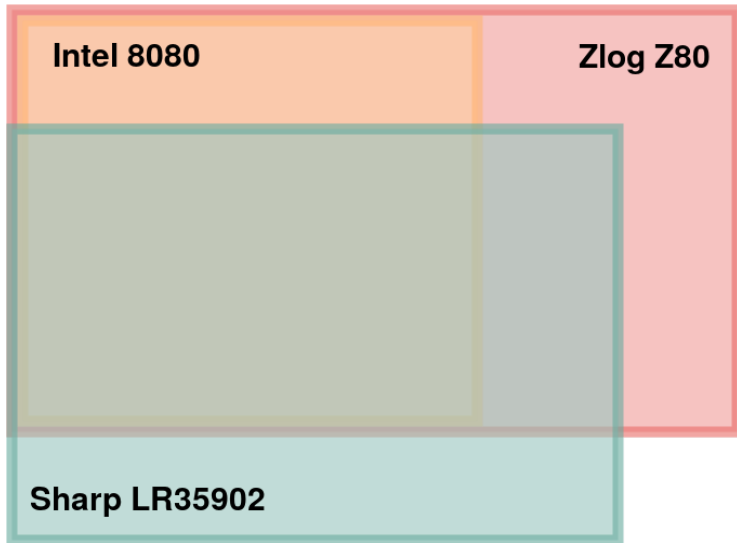
- ▶ CPU;

It is a eighth bits Sharp LR35902 control process unit with eight registers. The clock frequency is 4.19 MHz.

- ▶ ROM;

There are 256 bytes of bootstrap on the device. The rest is on game cartridges (different size and content for each game).

## Set instruction



## Codes: registers

```
1 pub struct Registers {  
2     pub a: u8,  
3     pub b: u8,  
4     pub c: u8,  
5     pub d: u8,  
6     pub e: u8,  
7     pub f: FlagsRegister ,  
8     pub h: u8,  
9     pub l: u8,  
10 }
```

[4]

## Codes: instructions

```
1 pub enum Instruction {  
2     INC(IncDecTarget),  
3     DEC(IncDecTarget),  
4     ADD(ArithmeticTarget),  
5     SUB(ArithmeticTarget),  
6  
7     AND(ArithmeticTarget),  
8     OR(ArithmeticTarget),  
9     XOR(ArithmeticTarget),  
10  
11    JP(JumpTest),  
12  
13    LD(LoadType),  
14  
15    PUSH(StackTarget),  
16    POP(StackTarget),  
17  
18    HALT,  
19    NOP,  
20 }
```

## Codes: instructions mapping

```
1 0x87 => Some( Instruction :: ADD( ArithmeticTarget :: A ) ) ,
2 0x80 => Some( Instruction :: ADD( ArithmeticTarget :: B ) ) ,
3 0x81 => Some( Instruction :: ADD( ArithmeticTarget :: C ) ) ,
4 0x82 => Some( Instruction :: ADD( ArithmeticTarget :: D ) ) ,
5 0x83 => Some( Instruction :: ADD( ArithmeticTarget :: E ) ) ,
6 0x84 => Some( Instruction :: ADD( ArithmeticTarget :: H ) ) ,
7 0x85 => Some( Instruction :: ADD( ArithmeticTarget :: L ) ) ,
8 0x86 => Some( Instruction :: ADD( ArithmeticTarget :: HLI ) ) ,
9 0xc6 => Some( Instruction :: ADD( ArithmeticTarget :: D8 ) ) ,
10
11 0x97 => Some( Instruction :: SUB( ArithmeticTarget :: A ) ) ,
12 0x90 => Some( Instruction :: SUB( ArithmeticTarget :: B ) ) ,
13 0x91 => Some( Instruction :: SUB( ArithmeticTarget :: C ) ) ,
14 0x92 => Some( Instruction :: SUB( ArithmeticTarget :: D ) ) ,
15 0x93 => Some( Instruction :: SUB( ArithmeticTarget :: E ) ) ,
16 0x94 => Some( Instruction :: SUB( ArithmeticTarget :: H ) ) ,
17 0x95 => Some( Instruction :: SUB( ArithmeticTarget :: L ) ) ,
18 0x96 => Some( Instruction :: SUB( ArithmeticTarget :: HLI ) ) ,
19 0xd6 => Some( Instruction :: SUB( ArithmeticTarget :: D8 ) ) ,
```

[4]

## Codes: memory bus

```
1 pub struct MemoryBus {
2     boot_rom: Option<[u8; BOOT_ROM_SIZE]>,
3     rom_bank_0: [u8; ROM_BANK_0_SIZE],
4     rom_bank_n: [u8; ROM_BANK_N_SIZE],
5     external_ram: [u8; EXTERNAL_RAM_SIZE],
6     working_ram: [u8; WORKING_RAM_SIZE],
7     zero_page: [u8; ZERO_PAGE_SIZE],
8     pub gpu: GPU,
9     pub interrupt_enable: InterruptFlags,
10    pub interrupt_flag: InterruptFlags,
11    timer: Timer,
12    divider: Timer,
13    pub joypad: Joypad,
14 }
```

[4]

# Conclusion

The emulation depends drastically on original and target systems.

- ▶ Reverse engineering → knowledge

This is the main job to develop an emulator.

- ▶ Two ways to implement the solution

It is mandatory to select the best option to emulate the original device: hardware or software (most common).

*Due to technological limitations and lack of knowledge, the emulation of all devices is not necessarily possible !*



Do you have any questions ?

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